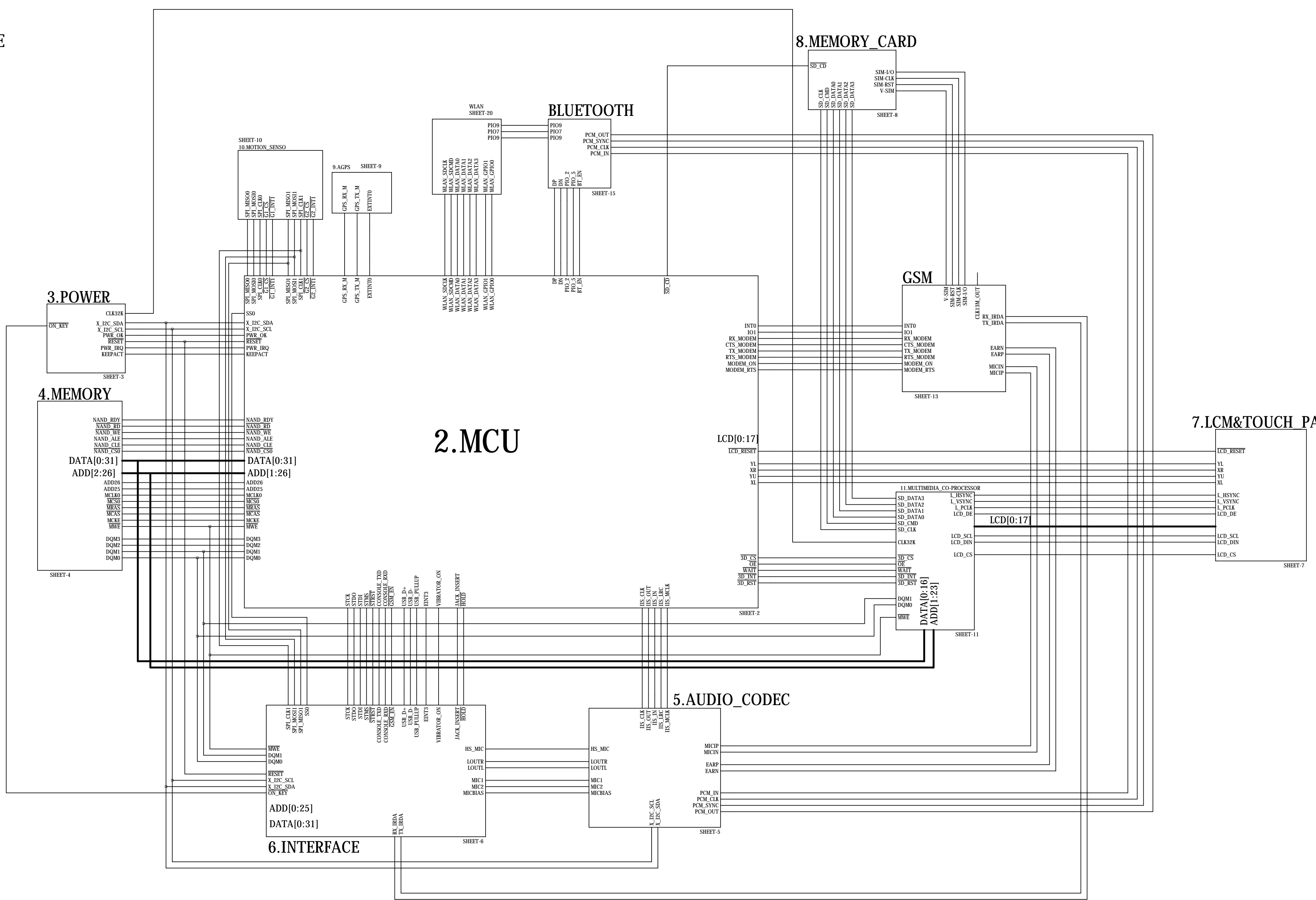
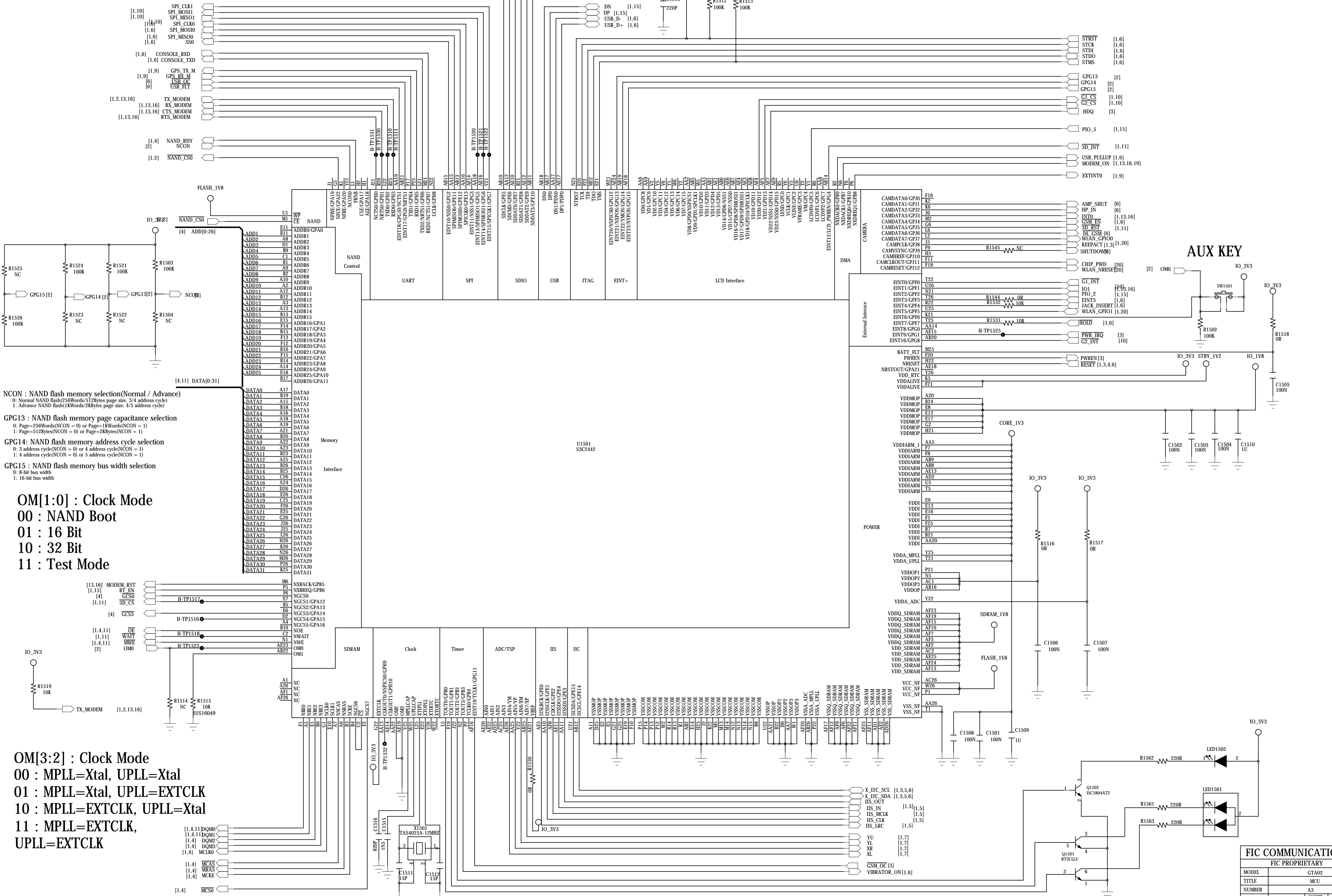


STRUCTURE



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	STRUCTURE		
NUMBER	A3		
DWG.	Allen Chang	SHEET DATE	1 of 200

MCU LOCATION: 15XX



NCON : NAND flash memory selection(Normal / Advance)
 0: Normal NAND flash(256Words/512Bytes page size, 3/4 address cycle)
 1: Advance NAND flash(1KWWords/2KBytes page size, 4/5 address cycle)

GPG13 : NAND flash memory page capacitance selection
 0: Page=256Words(NCON = 0) or Page=1KWords(NCON = 1)
 1: Page=512Bytes(NCON = 0) or Page=2KBytes(NCON = 1)

GPG14 : NAND flash memory address cycle selection
 0: 3 address cycle(NCON = 0) or 4 address cycle(NCON = 1)
 1: 4 address cycle(NCON = 0) or 5 address cycle(NCON = 1)

GPG15 : NAND flash memory bus width selection
 0: 8-bit bus width
 1: 16-bit bus width

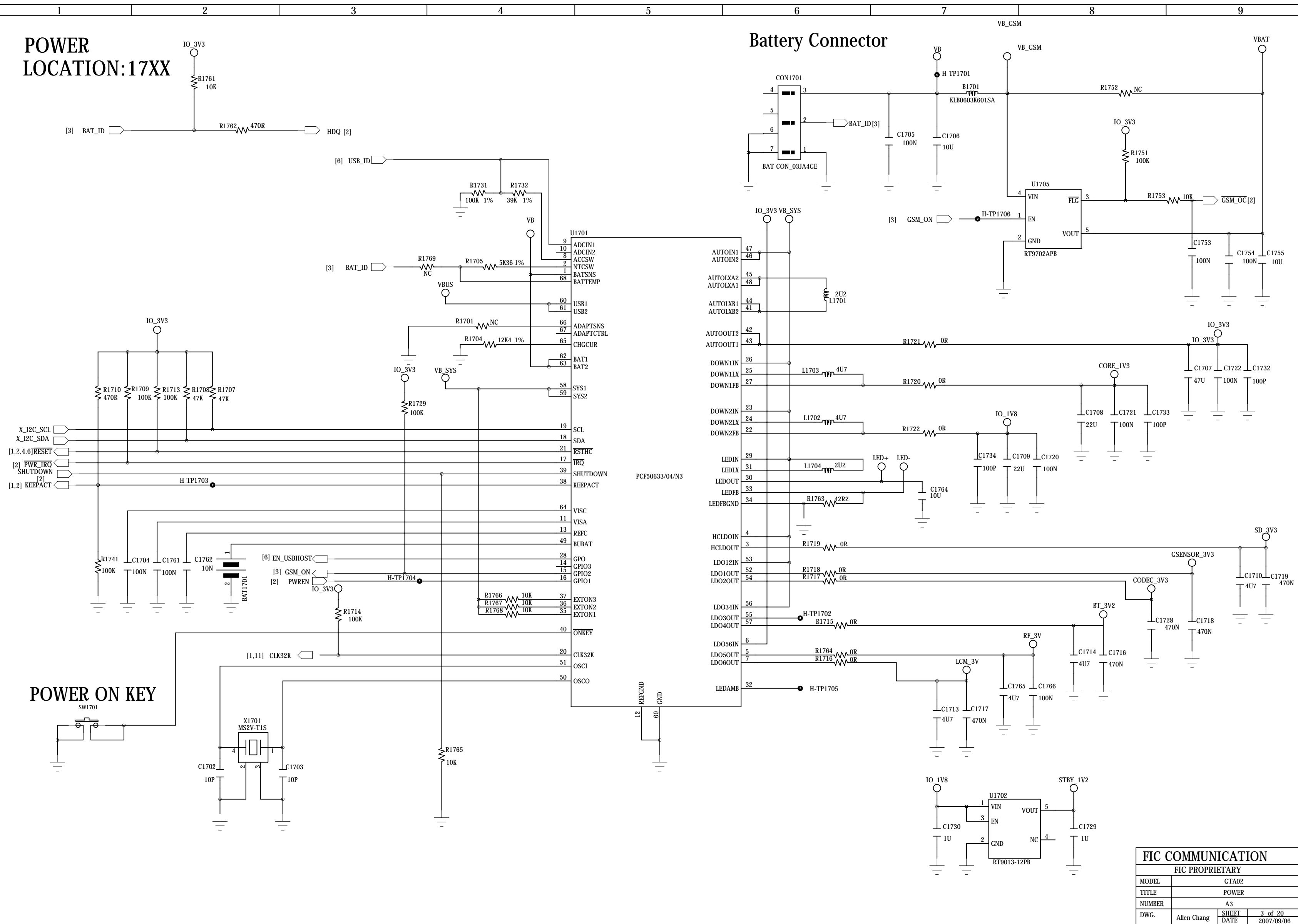
OM[1:0] : Clock Mode
 00 : NAND Boot
 01 : 16 Bit
 10 : 32 Bit
 11 : Test Mode

OM[3:2] : Clock Mode
 00 : MPLL=Xtal, UPLL=Xtal
 01 : MPLL=Xtal, UPLL=EXTCLK
 10 : MPLL=EXTCLK, UPLL=Xtal
 11 : MPLL=EXTCLK, UPLL=EXTCLK

FIC COMMUNICATION	
FIC PROPRIETARY	
MODEL	GT402
TITLE	MCU
NUMBER	A3
DWG.	SHEET 2 of 2007
Allen Chang	

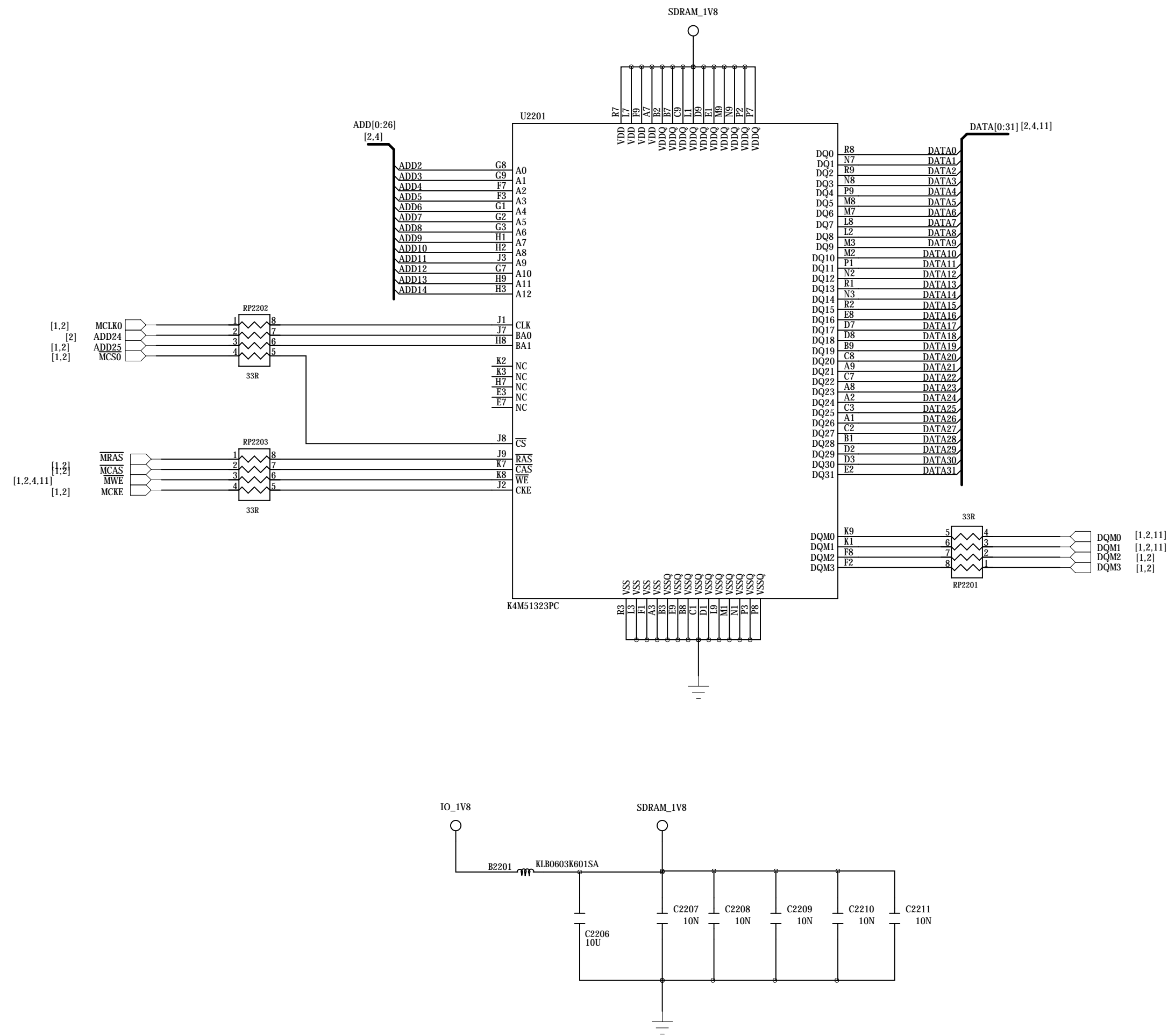
POWER LOCATION: 17XX

Battery Connector

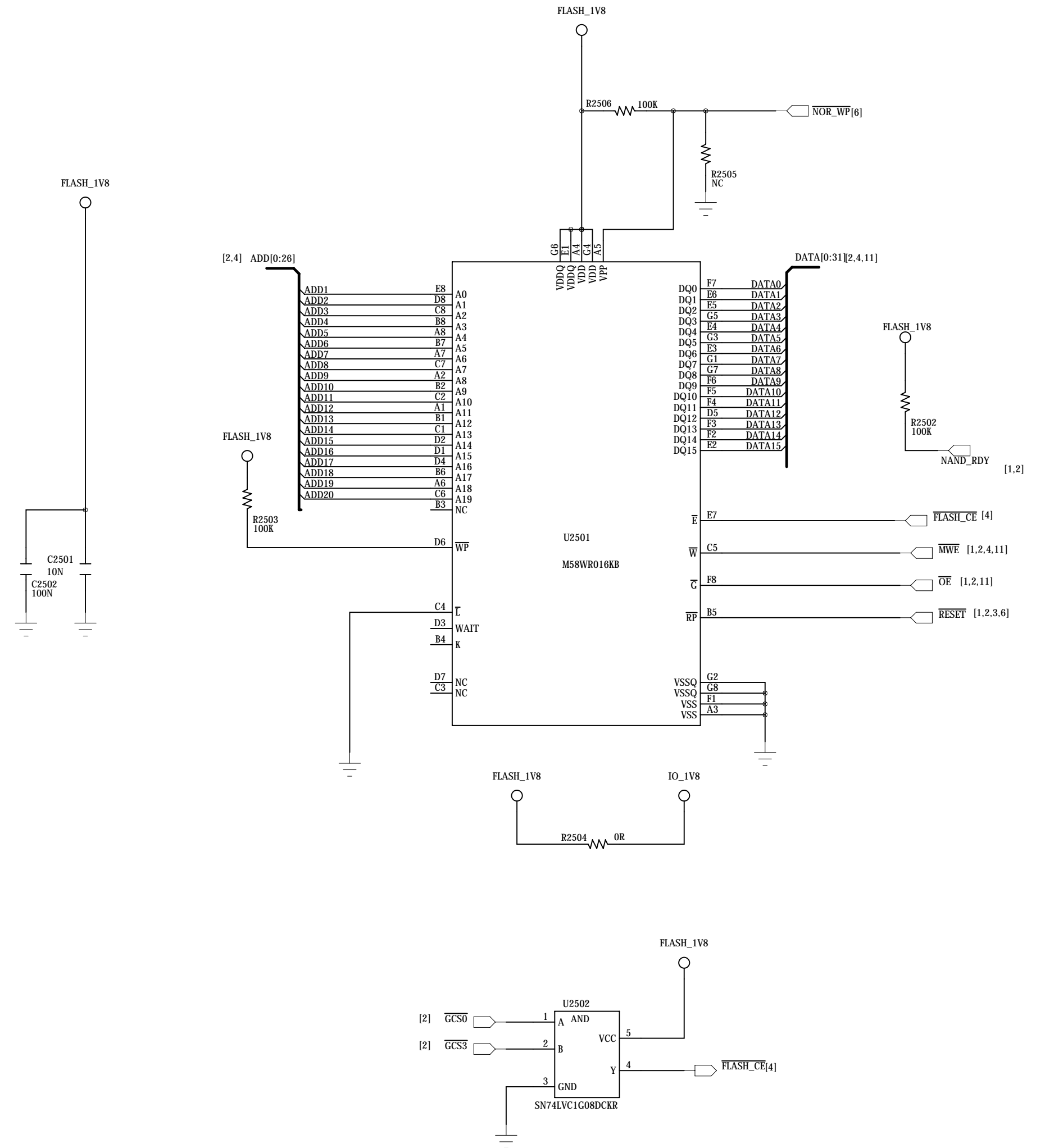


FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	POWER		
NUMBER	A3		
DWG.	Allen Chang	SHEET	3 of 20
		DATE	2007/09/06

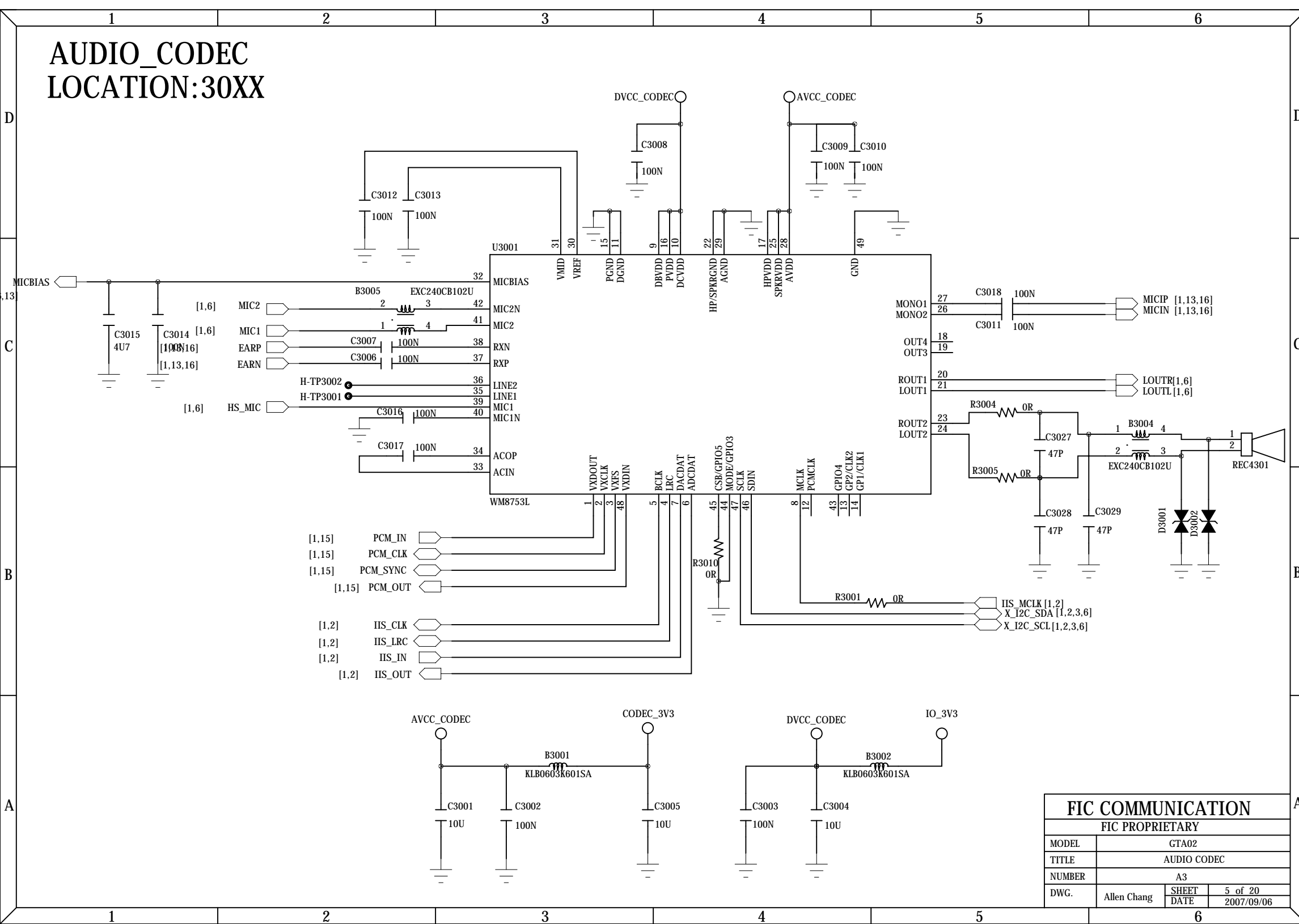
SDRAM
LOCATION: 22XX



NOR FLASH
LOCATION: 25XX

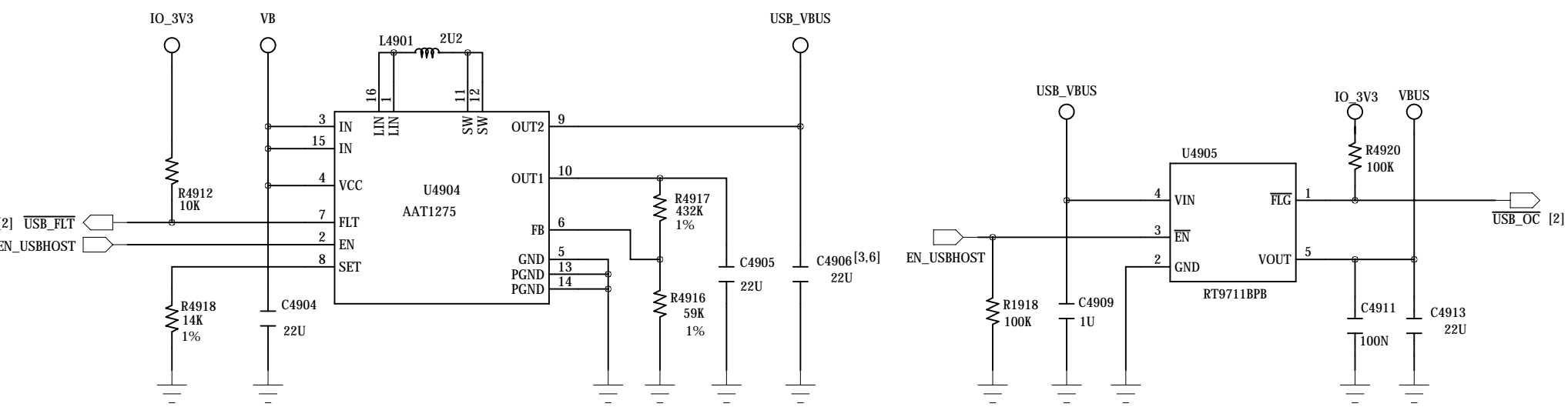
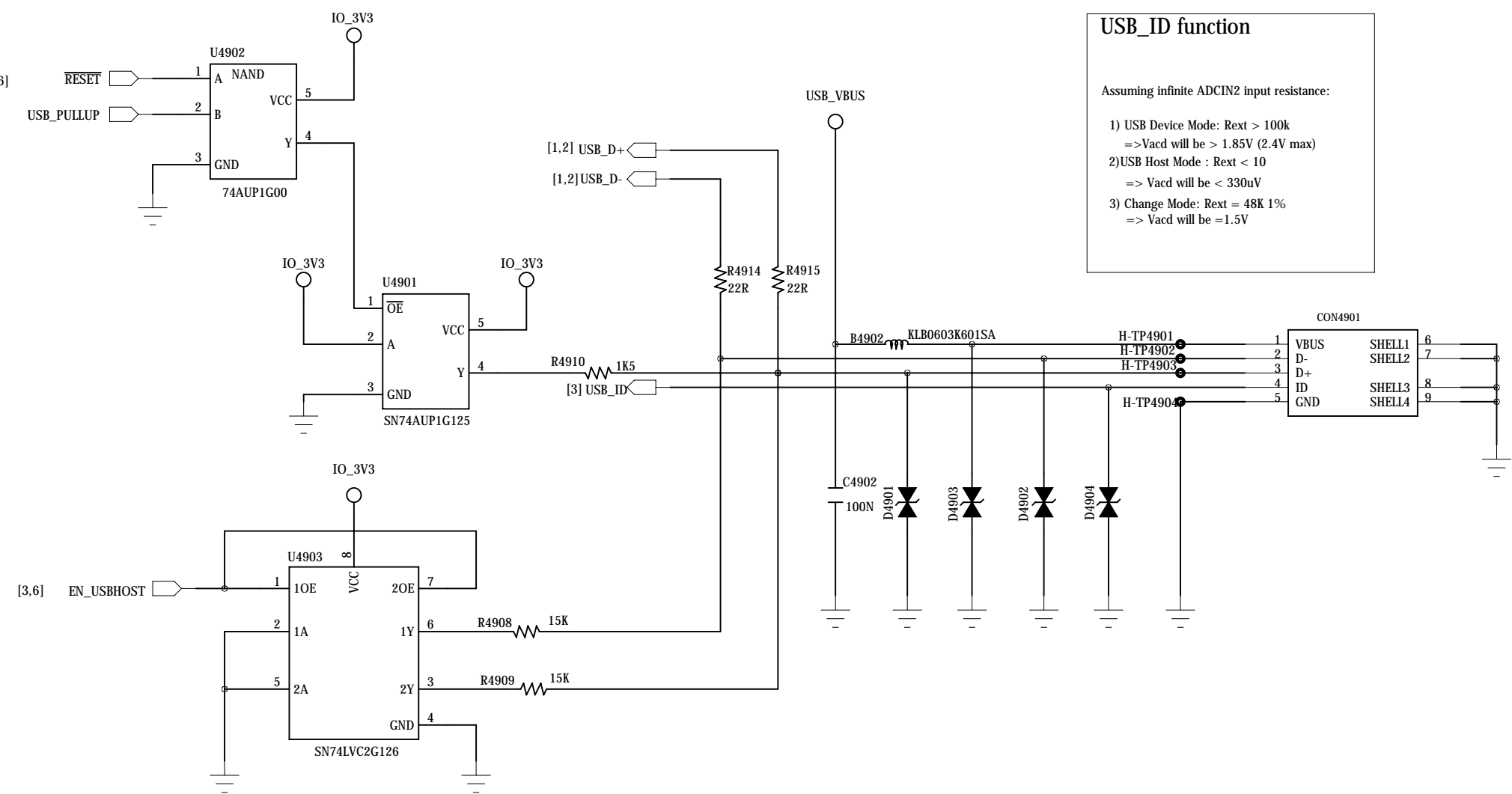


AUDIO_CODEC LOCATION:30XX

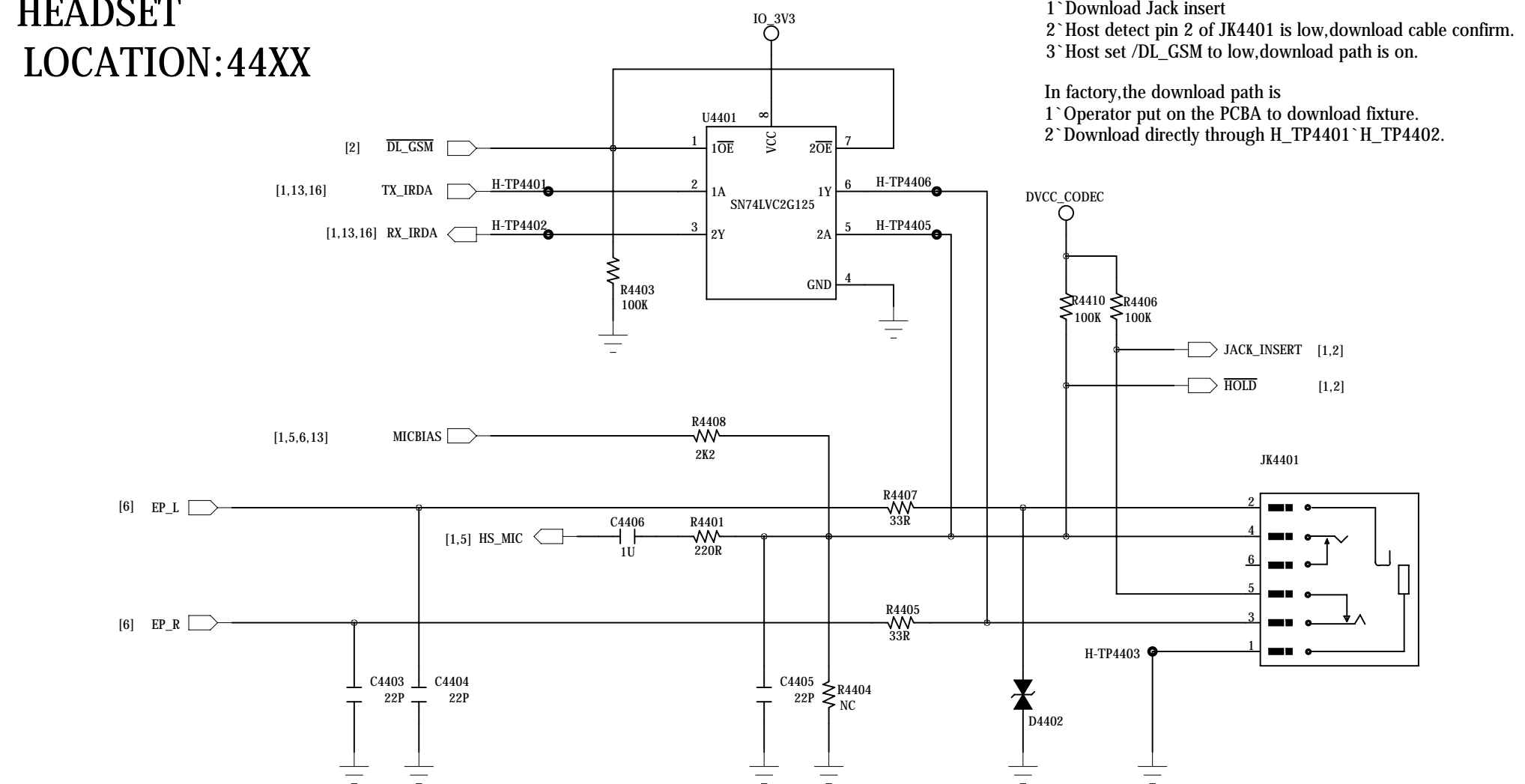


FIC COMMUNICATION	
FIC PROPRIETARY	
MODEL	GTA02
TITLE	AUDIO CODEC
NUMBER	A3
DWG.	Allen Chang
SHEET	5 of 20
DATE	2007/09/06

USB LOCATION: 49XX



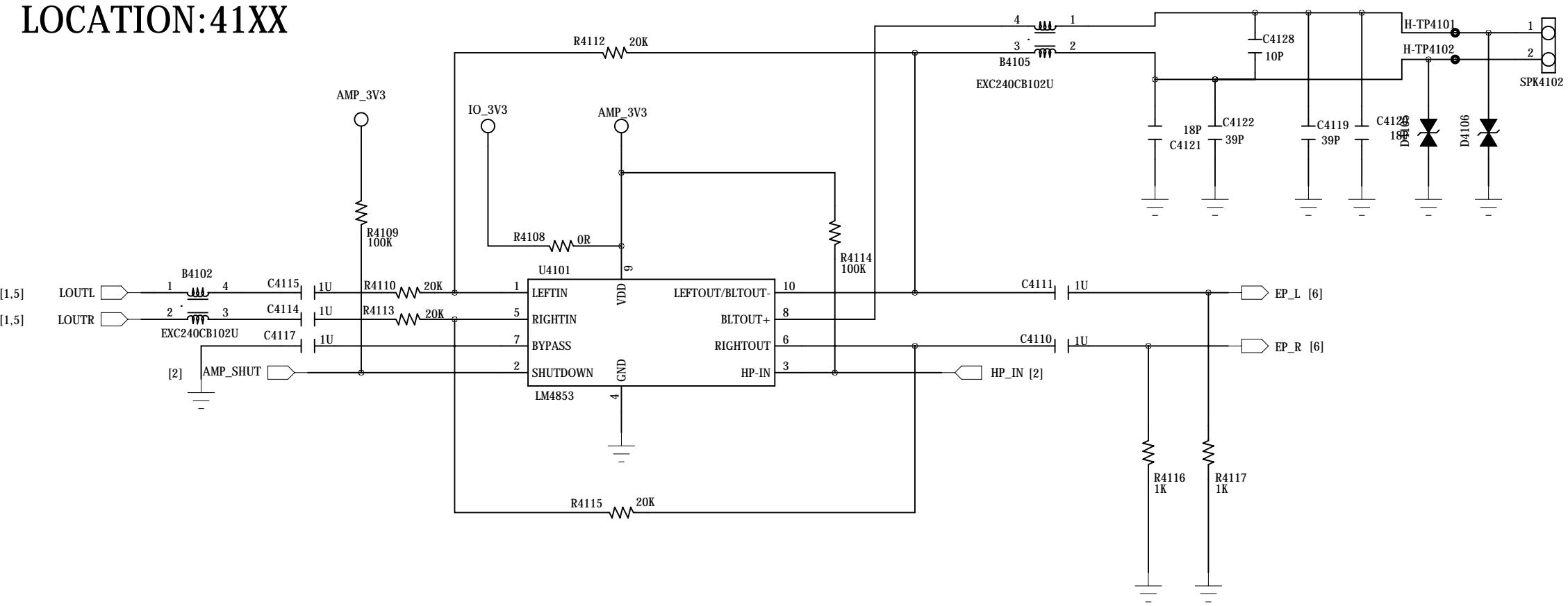
HEADSET LOCATION: 44XX



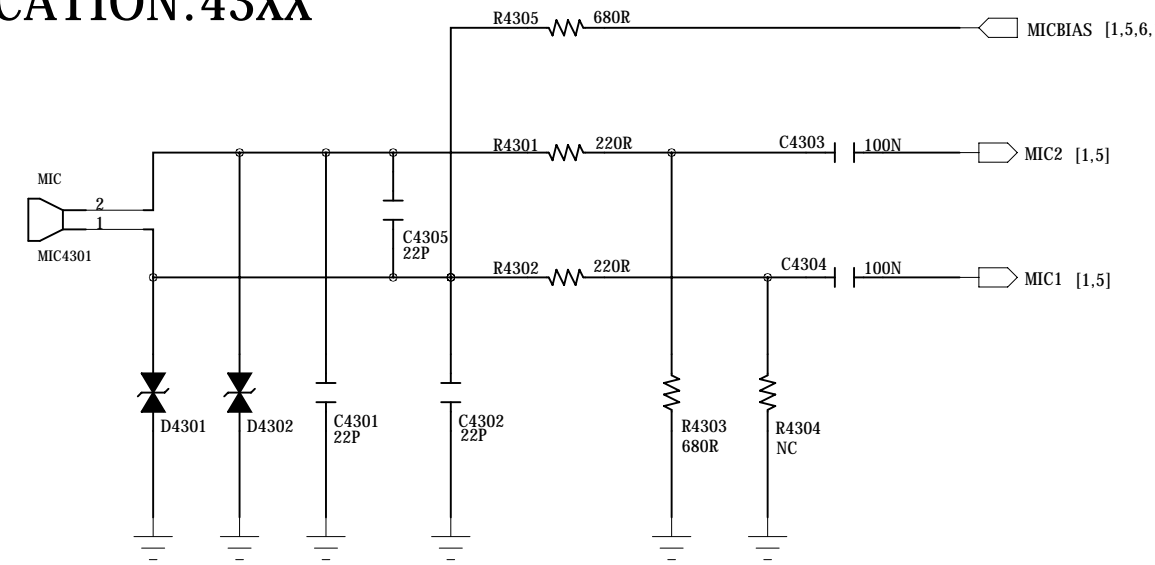
In RD side, the download path is
 1` Download Jack insert
 2` Host detect pin 2 of JK4401 is low,download cable confirm.
 3` Host set /DL_GSM to low,download path is on.

In factory,the download path is
 1` Operator put on the PCBA to download fixture.
 2` Download directly through H_TP4401`H_TP4402.

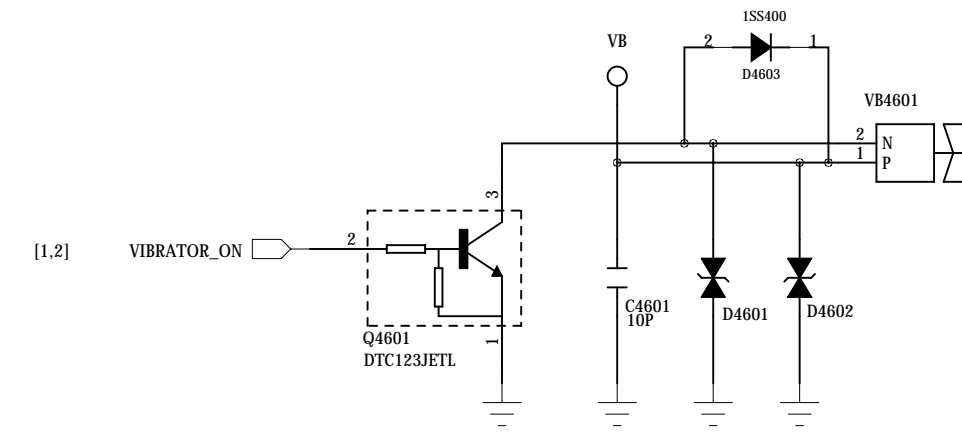
SPK LOCATION: 41XX



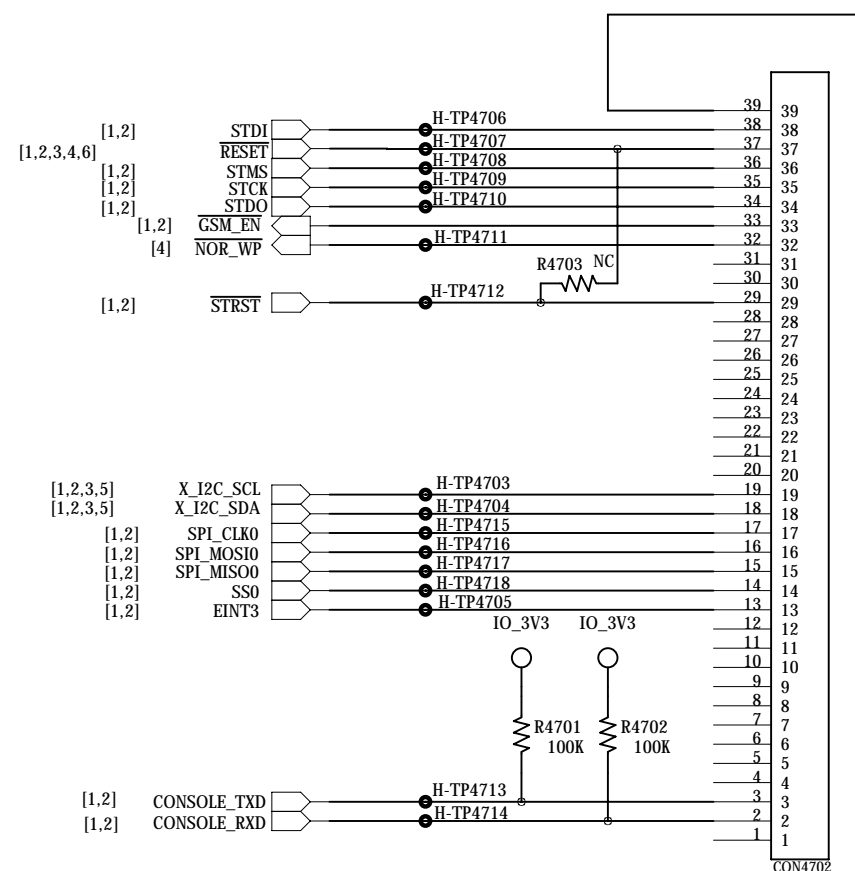
MIC LOCATION: 43XX



VIBRATOR LOCATION: 46XX

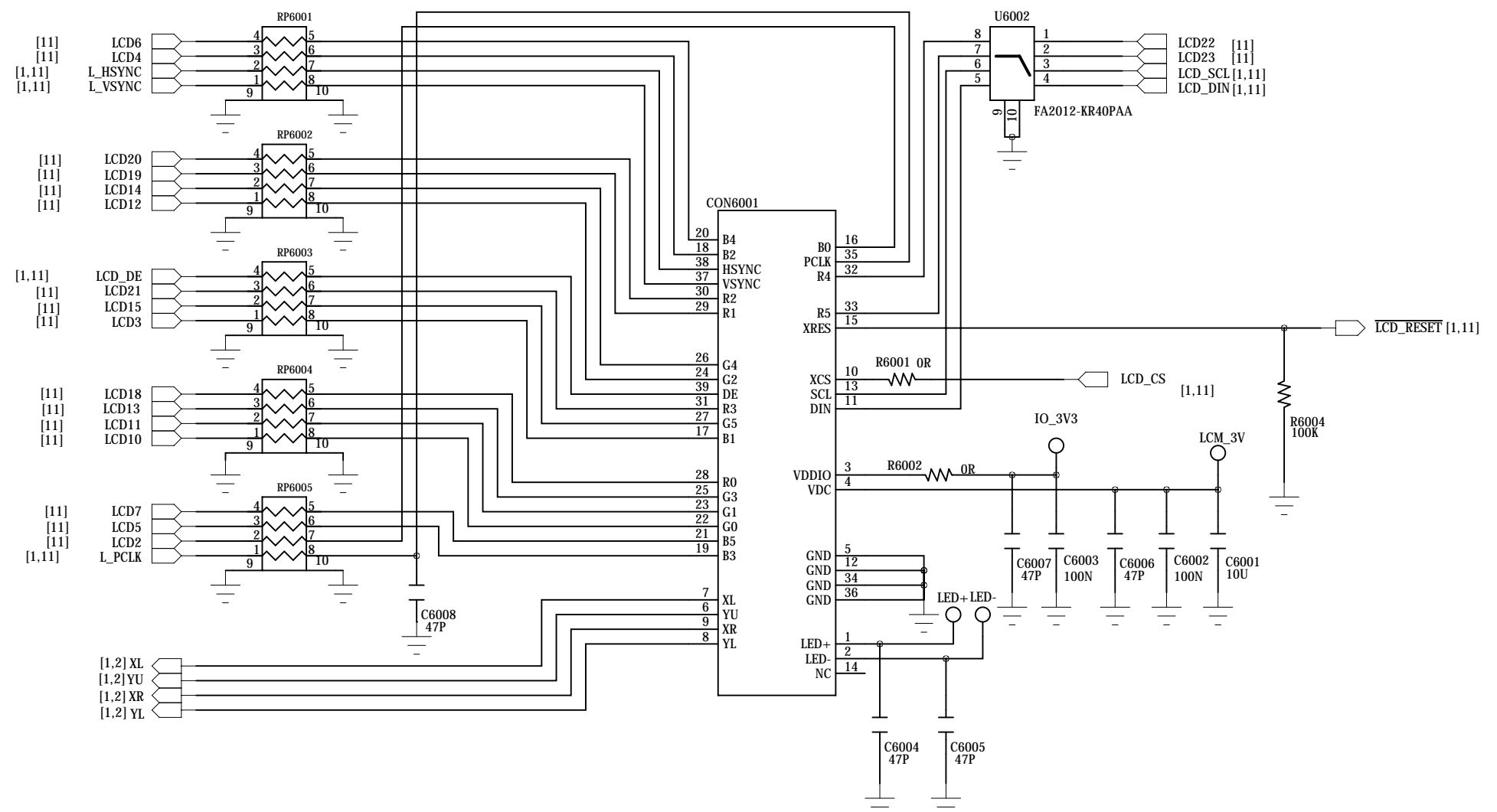


DEBUG LOCATION: 47XX



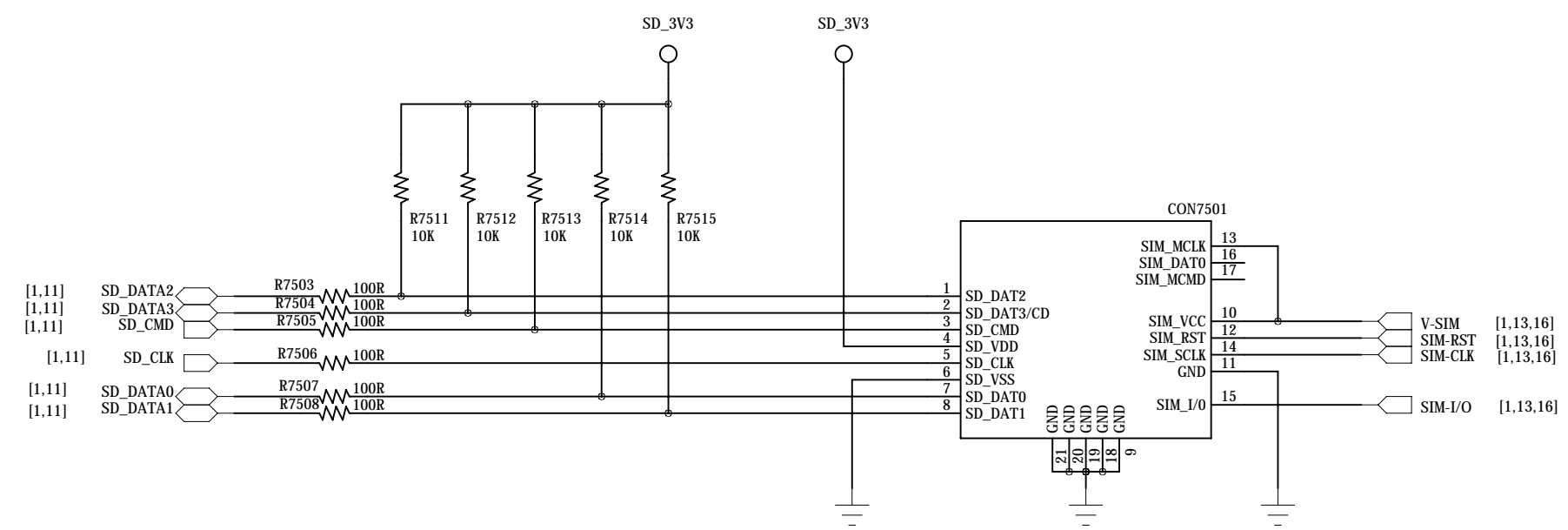
FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	INTERFACE		
NUMBER	A3		
DWG.	Allen Chang	SHEET	6
		DATE	20

LCM_TOUCH_PANEL
LOCATION:6XXX



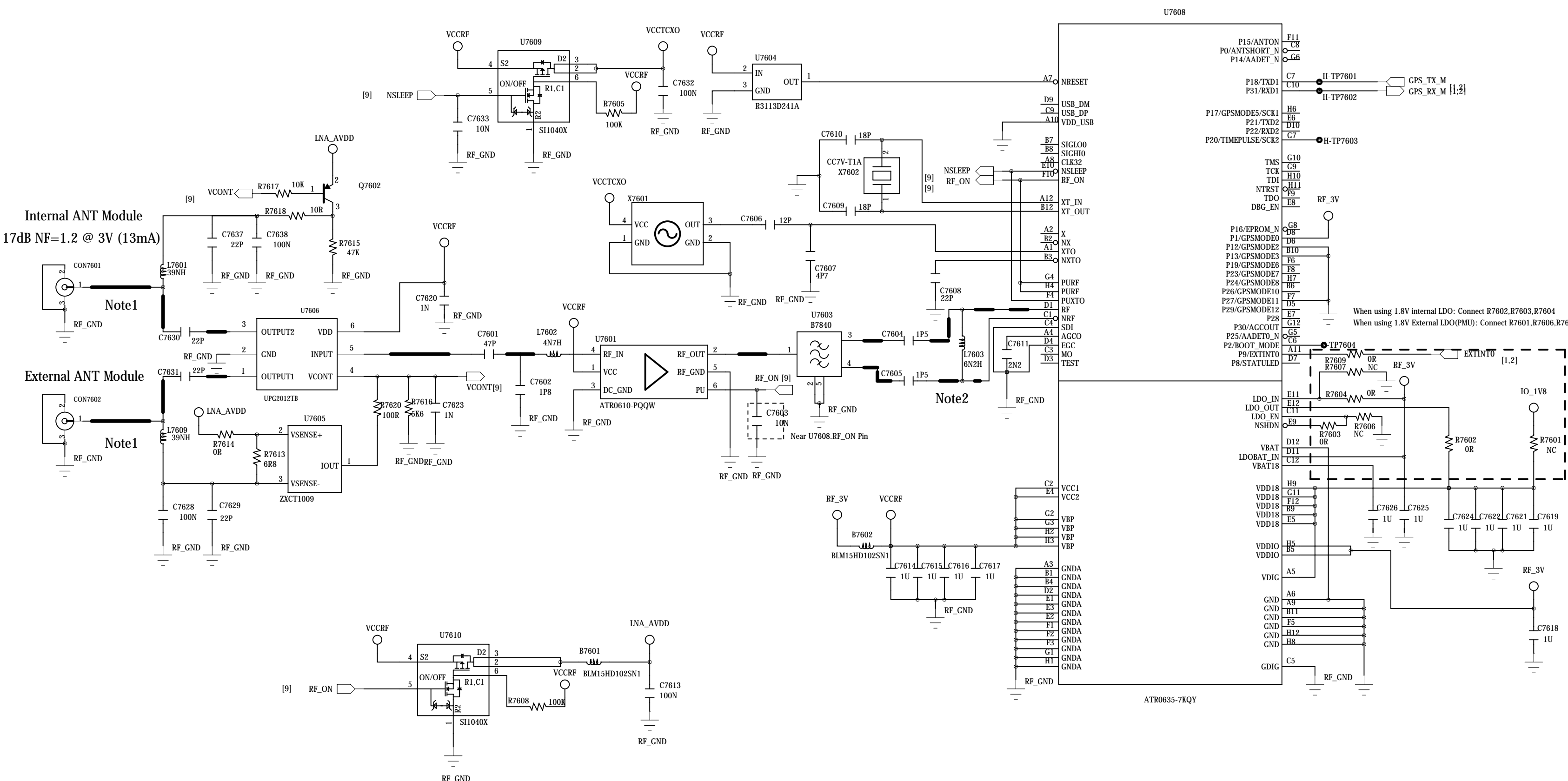
FIC COMMUNICATION	
FIC PROPRIETARY	
MODEL	GTA02
TITLE	LCM & TOUCH PANEL
NUMBER	A3
DWG.	Allen Chang
SHEET	7 of 20
DATE	2007/09/06

MEMORY_CARD
LOCATION: 75XX

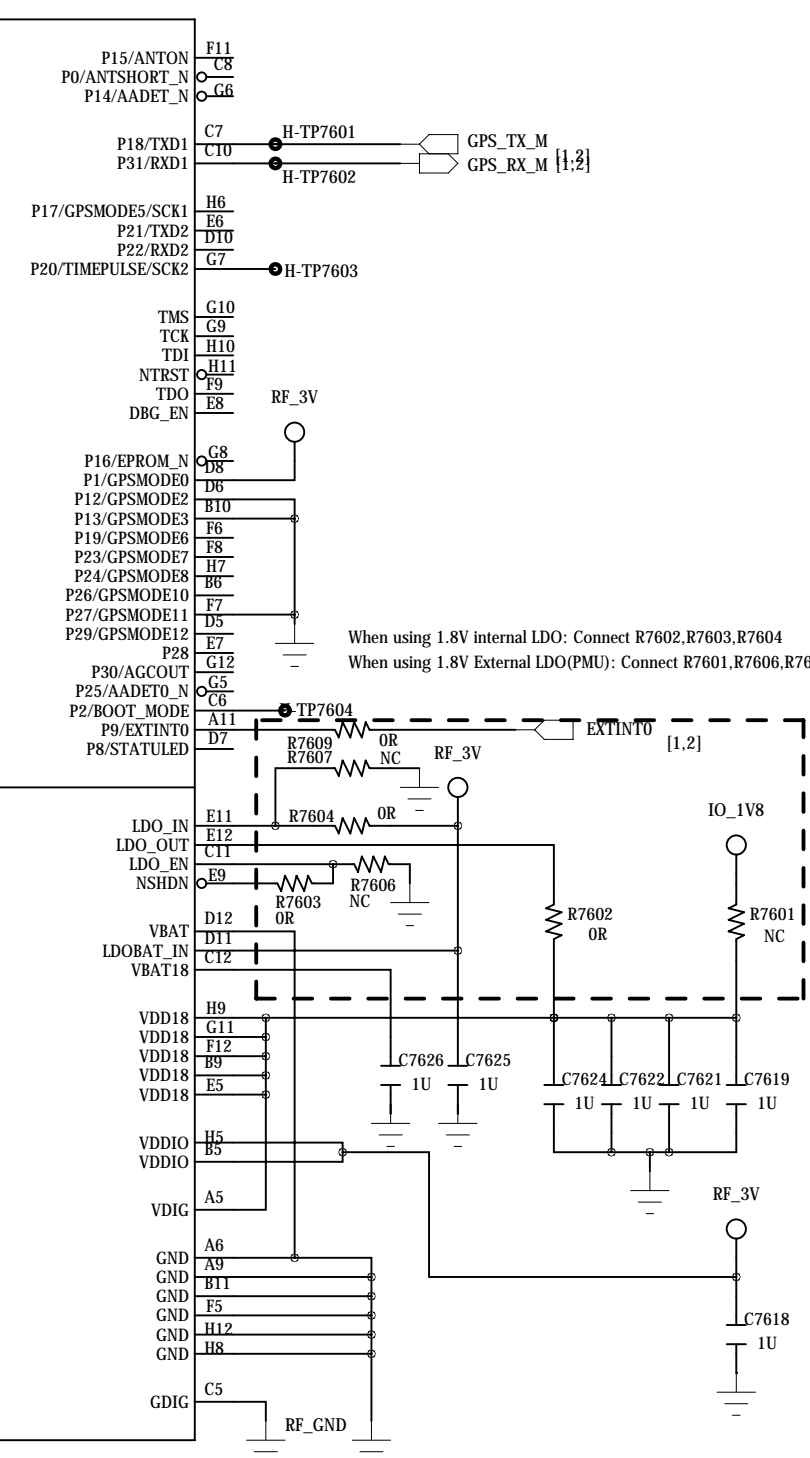


FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	MEMORY CARD		
NUMBER	A3		
DWG.	Allen Chang	SHEET	8 of 20
		DATE	2007/09/06

ublox_ANTARIS 4_ATR0635&ATR0610
LOCATION:76XX

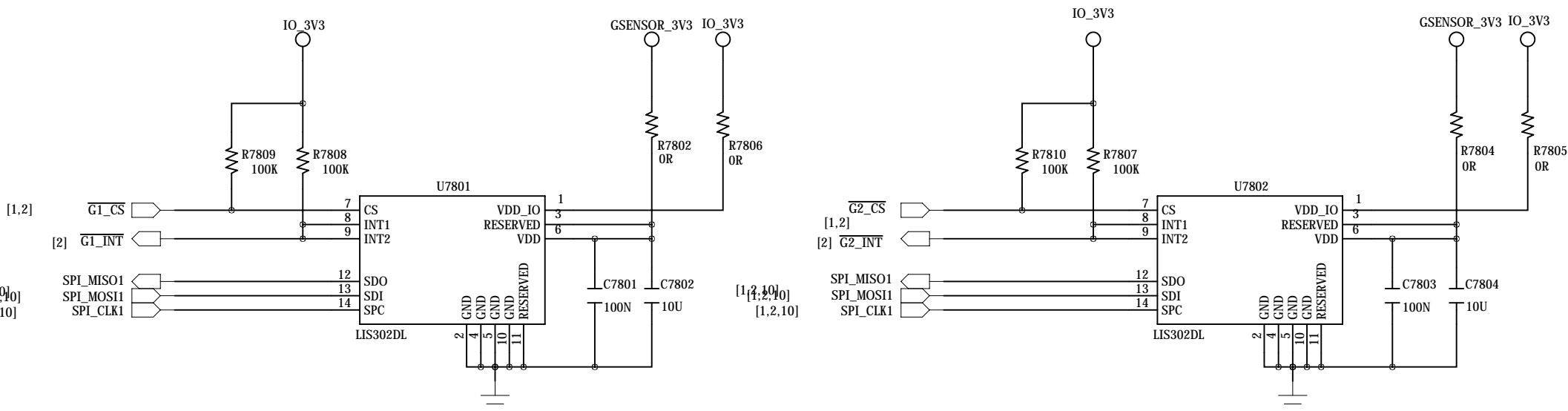


Note1: These traces are RF transmission line and the characteristic impedance is 50 ohm.
Note2: Differential RF traces and the balanced impedance is 100 ohm



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	AGPS		
NUMBER	A3		
DWG.	Shawn Lin	SHEET	9 of 20
		DATE	2007/09/06

MOTION_SENSOR
LOCATION: 78XX



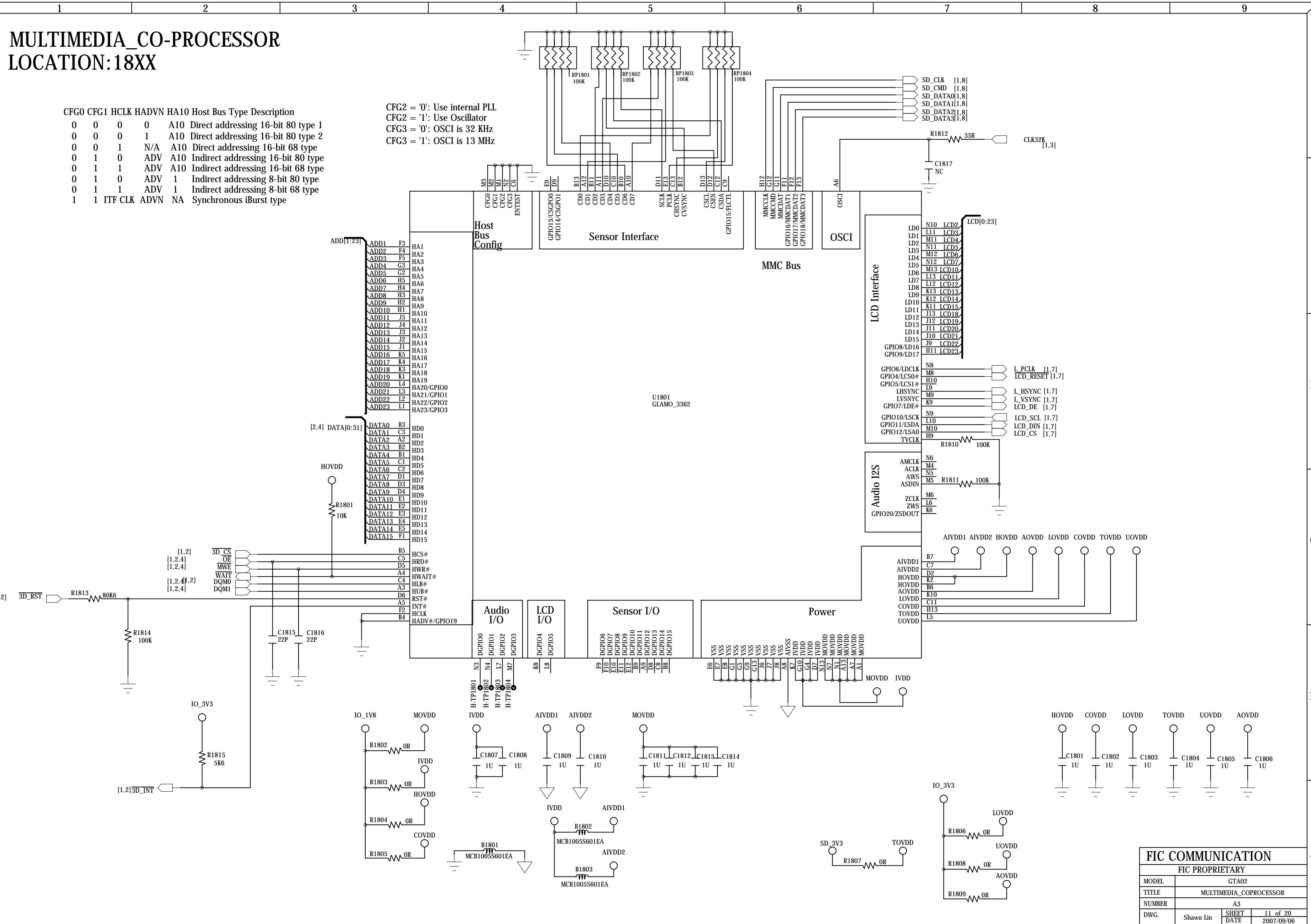
FIC COMMUNICATION	
FIC PROPRIETARY	
MODEL	GTA02
TITLE	MOTION_SENSOR
NUMBER	A3
DWG.	Allen Chang
	SHEET 10 of 20
	DATE 2007/09/06

MULTIMEDIA_CO-PROCESSOR LOCATION:18XX

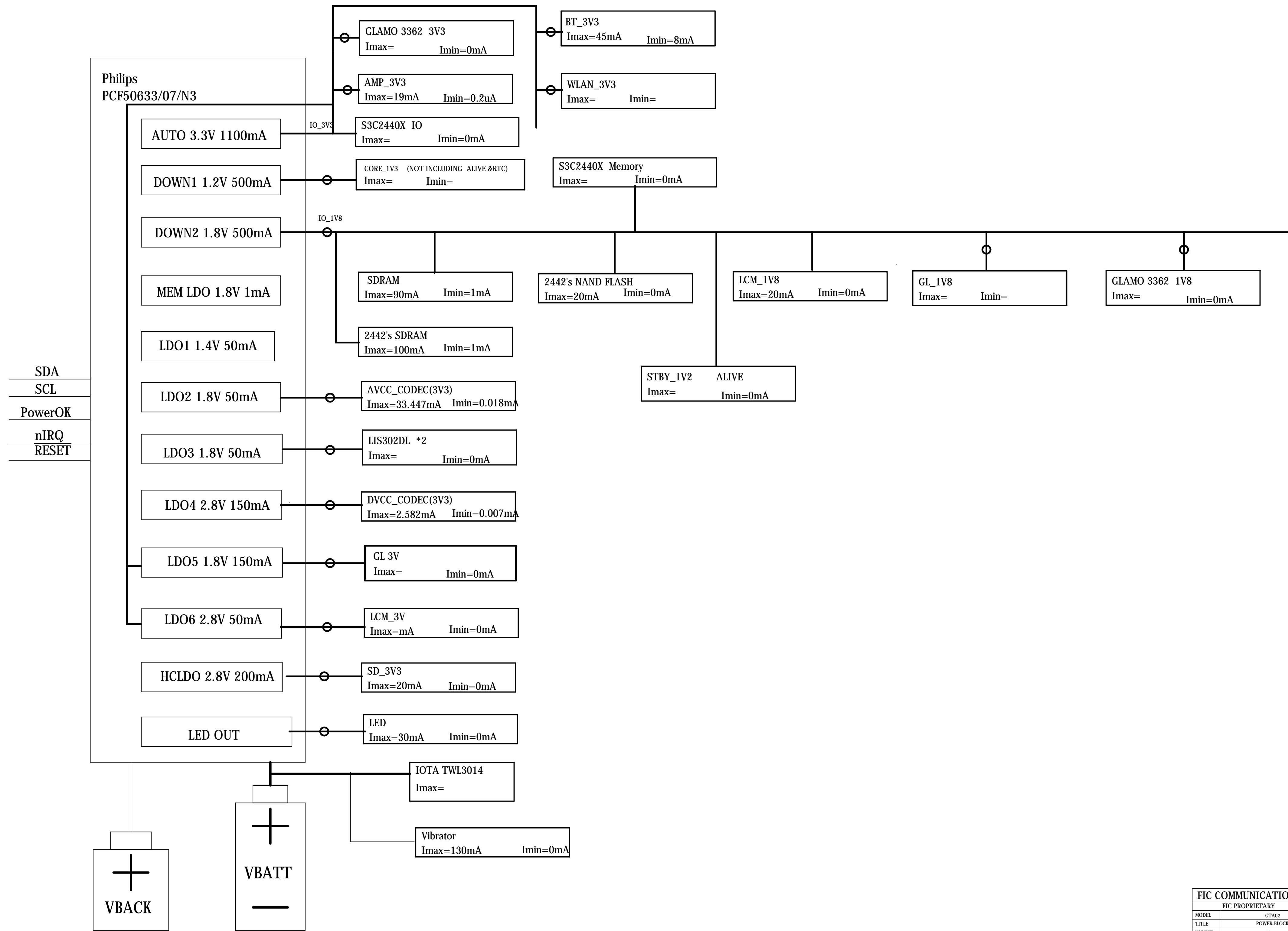
CFG0 CFG1 HCLK HADVN HA10 Host Bus Type Description

0	0	0	0	A10	Direct addressing 16-bit 80 type 1
0	0	0	1	A10	Direct addressing 16-bit 80 type 2
0	0	1	N/A	A10	Direct addressing 16-bit 68 type
0	1	0	ADV	A10	Indirect addressing 16-bit 80 type
0	1	1	ADV	A10	Indirect addressing 16-bit 68 type
0	1	0	ADV	1	Indirect addressing 8-bit 80 type
0	1	1	ADV	1	Indirect addressing 8-bit 68 type
1	1	ITF CLK	ADV N	A	Synchronous iBus type

CFG2 = '0': Use internal PLL
 CFG2 = '1': Use Oscillator
 CFG3 = '0': OSCI is 32 KHz
 CFG3 = '1': OSCI is 13 MHz

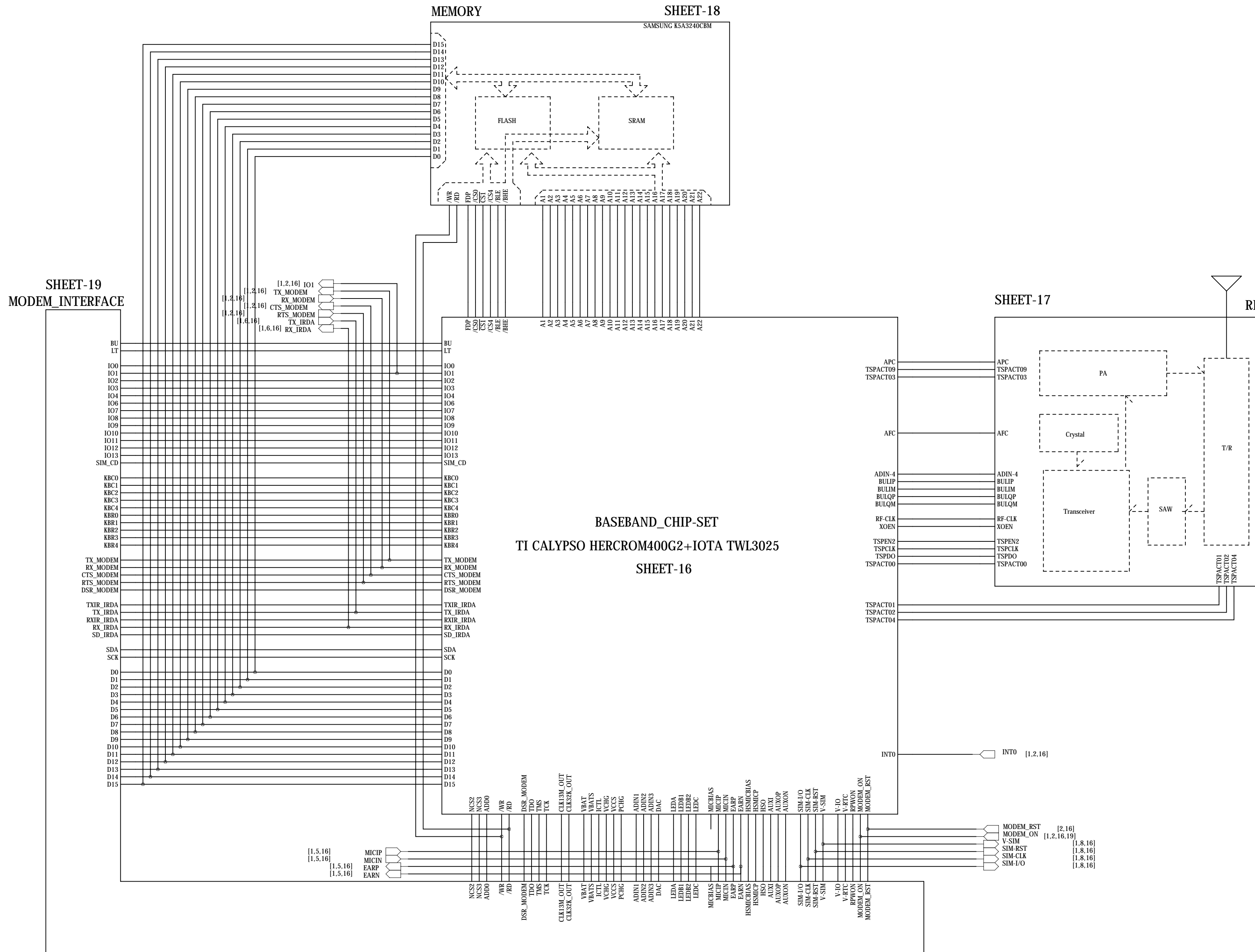


FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	MULTIMEDIA_CO-PROCESSOR		
NUMBER	A3		
DWG.	Shawn Lin	SHEET	11 of 20
		DATE	2007/09/06

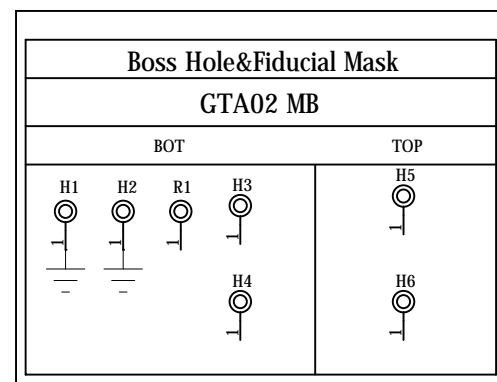
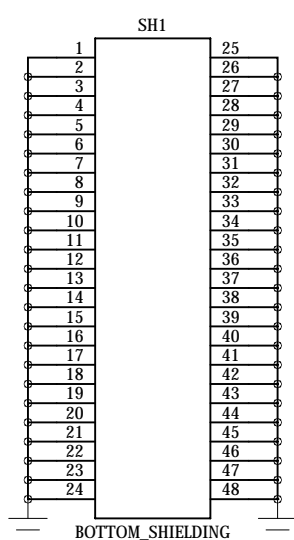
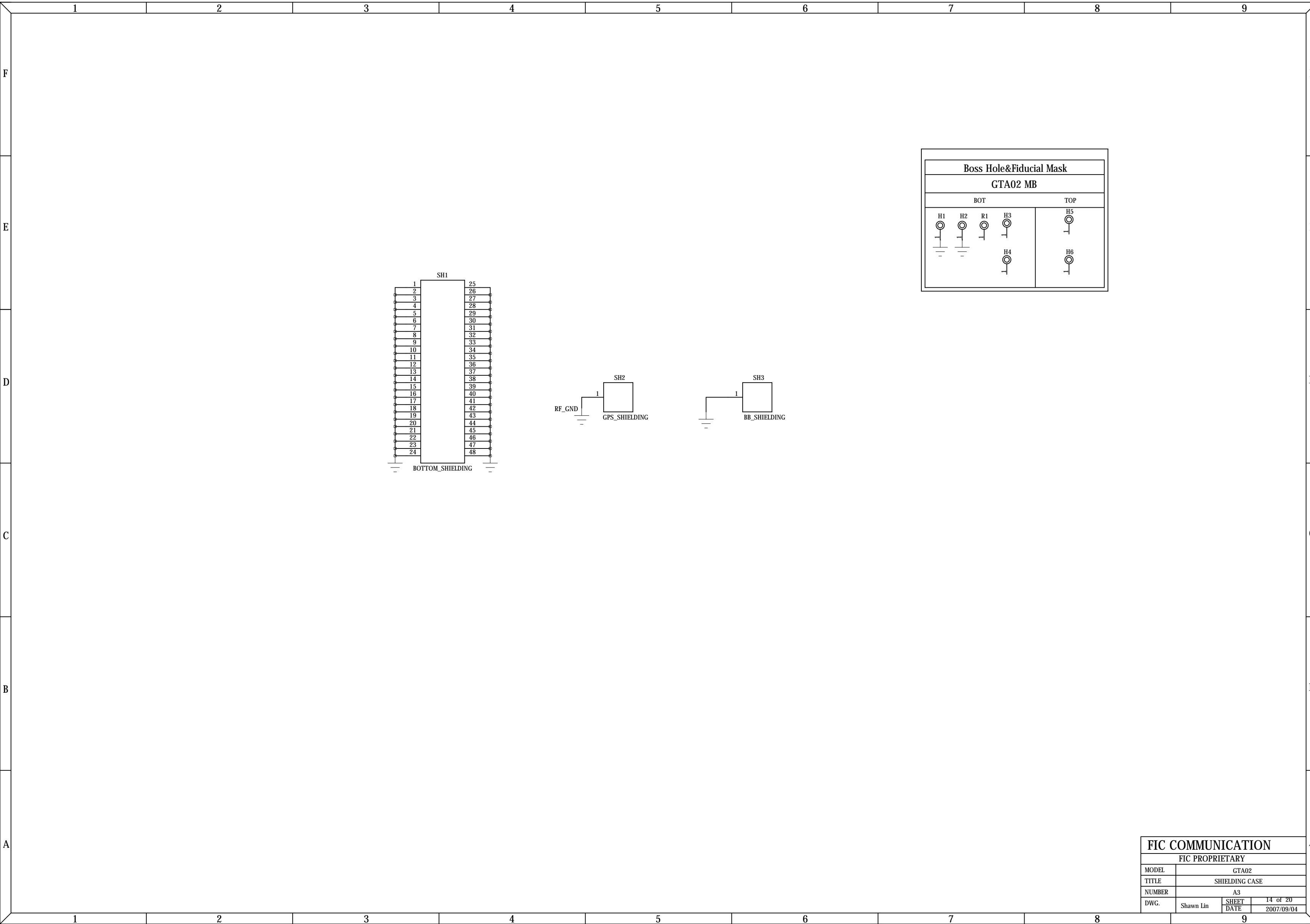


FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	POWER BLOCK		
NUMBER	A3		
DWG.	Allen Chang	SHEET	12
		DATE	20

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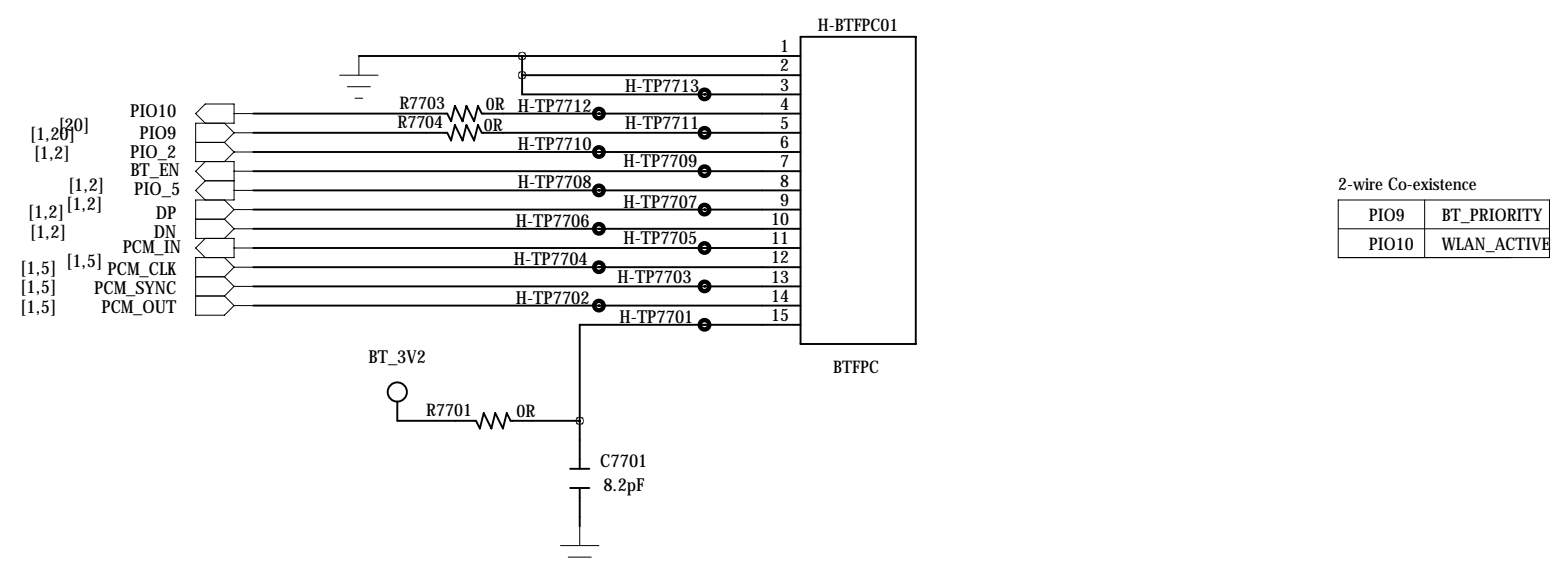


FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	BLOCK DIAGRAM		
NUMBER	A3		
DWG.	Shawn Lin	SHEET	13 OF 2007



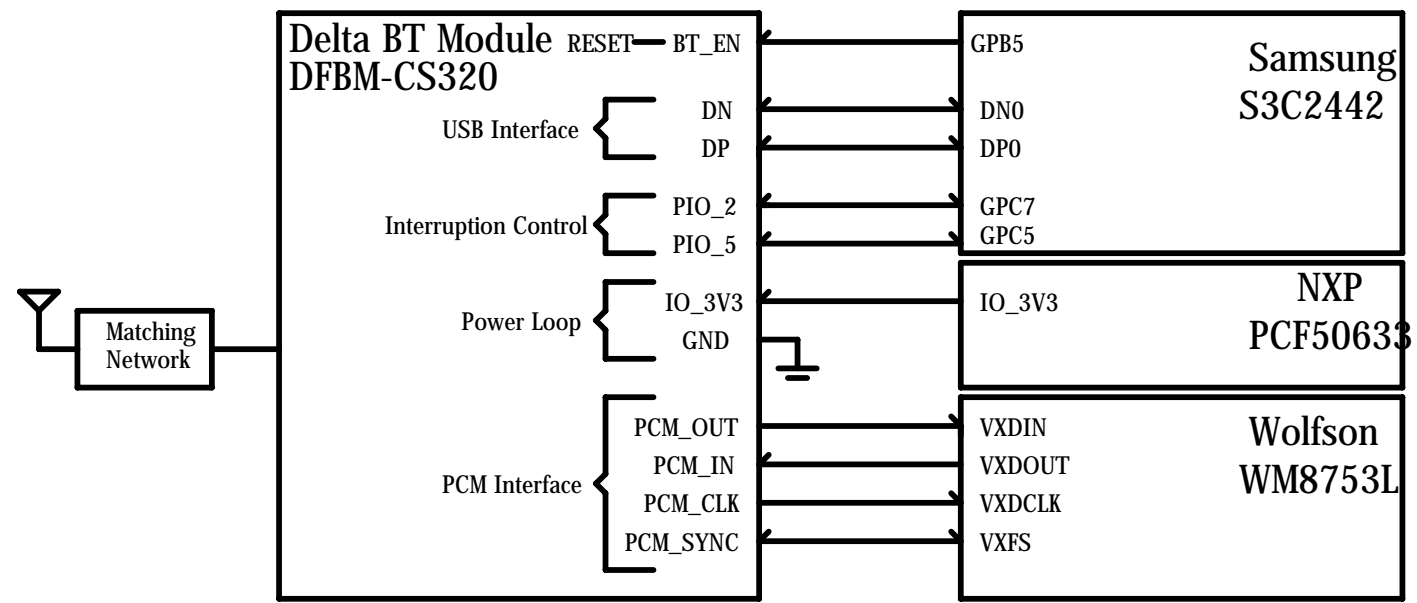
FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	SHIELDING CASE		
NUMBER	A3		
DWG.	Shawn Lin	SHEET	14 of 20
		DATE	2007/09/04

Bluetooth LOCATION: 77XX



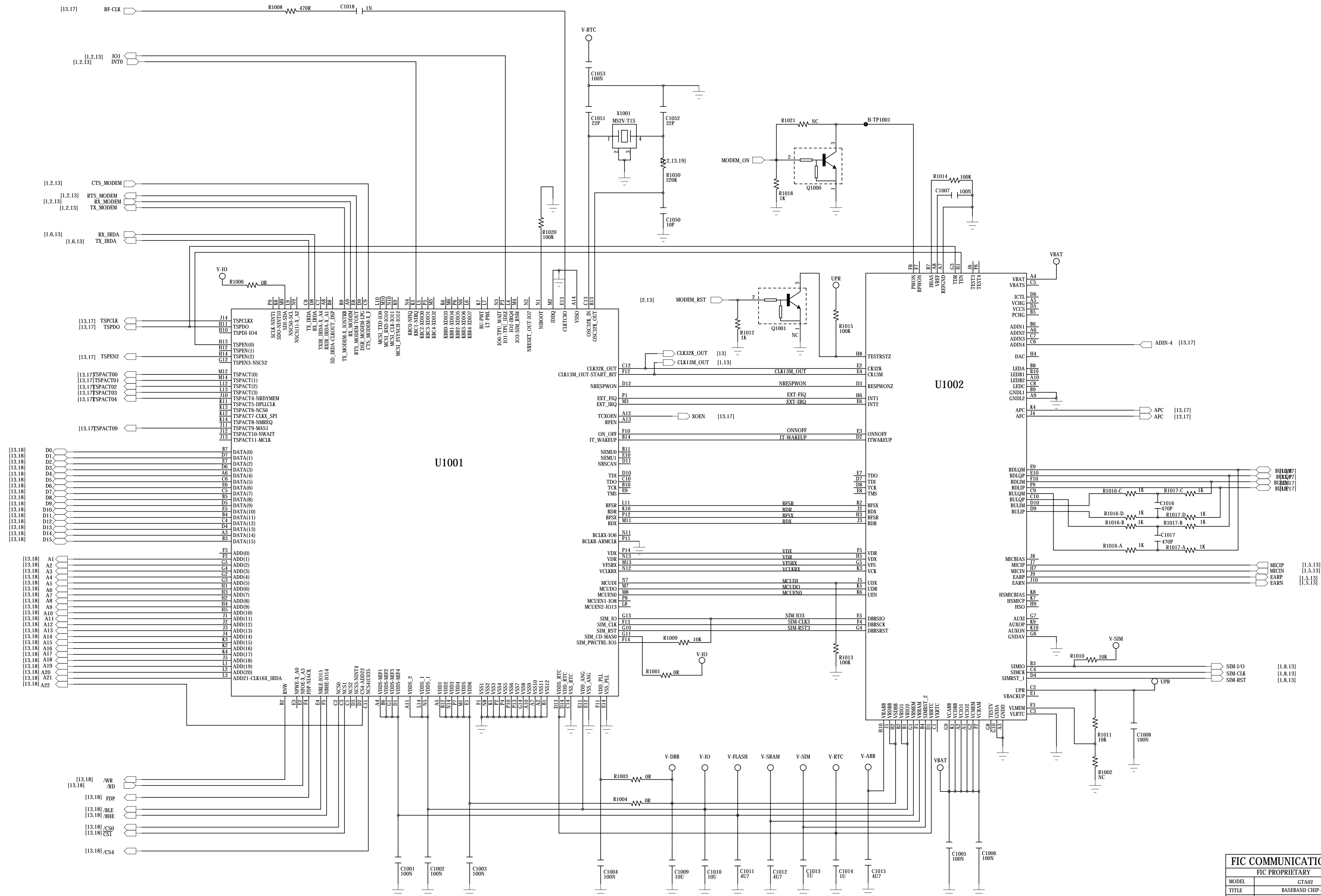
2-wire Co-existence

PIO9	BT_PRIORITY
PIO10	WLAN_ACTIVE



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	Bluetooth		
NUMBER	A3		
DWG.	Shawn Lin	SHEET DATE	15 of 20 2007/09/06

BASEBAND_CHIP LOCATION: 1XXX

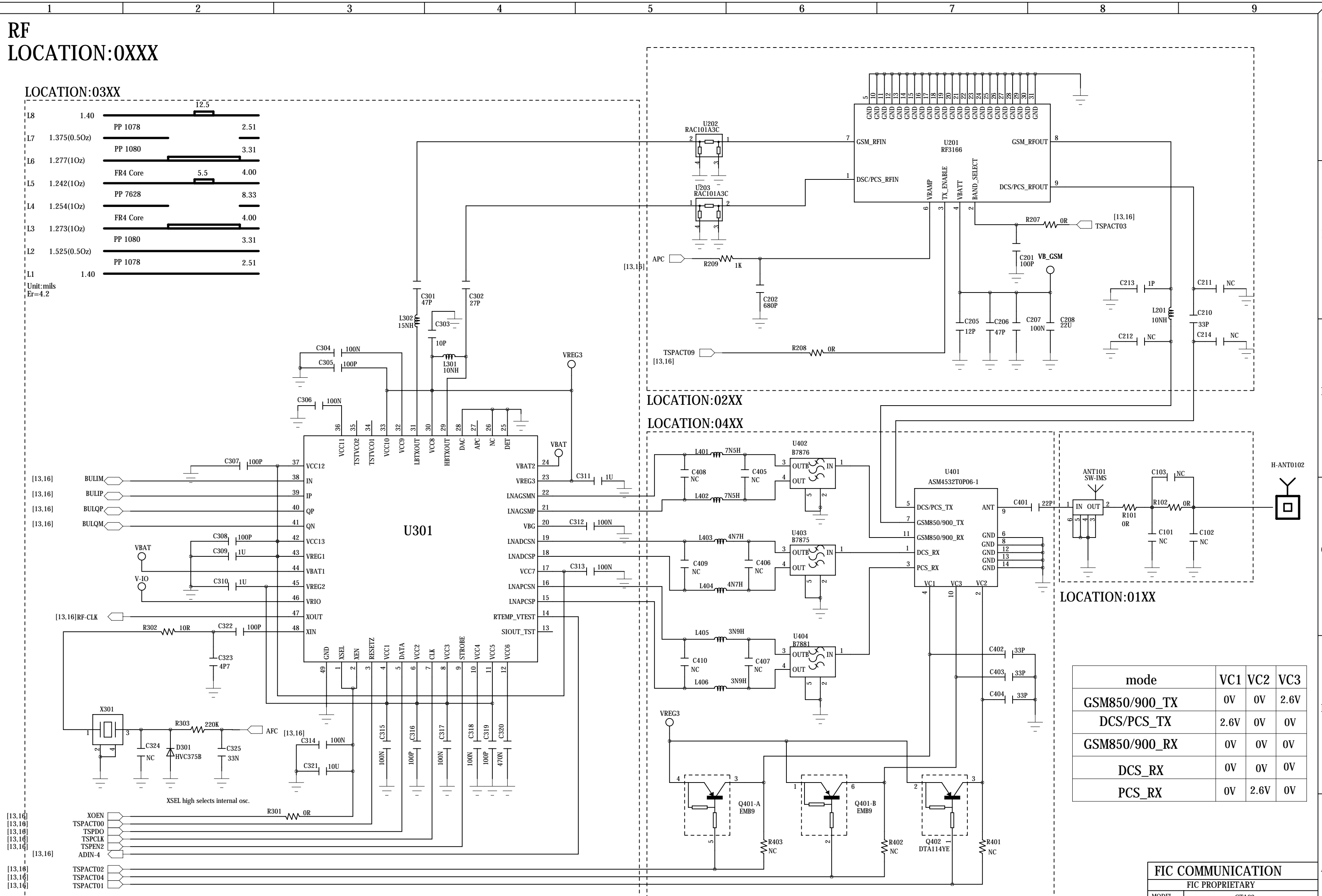


RF
LOCATION:0XXX

LOCATION:03XX

L8	1.40	12.5	
L7	1.375(0.50z)		2.51
L6	1.277(10z)		3.31
L5	1.242(10z)	5.5	4.00
L4	1.254(10z)		8.33
L3	1.273(10z)		4.00
L2	1.525(0.50z)		3.31
L1	1.40		2.51

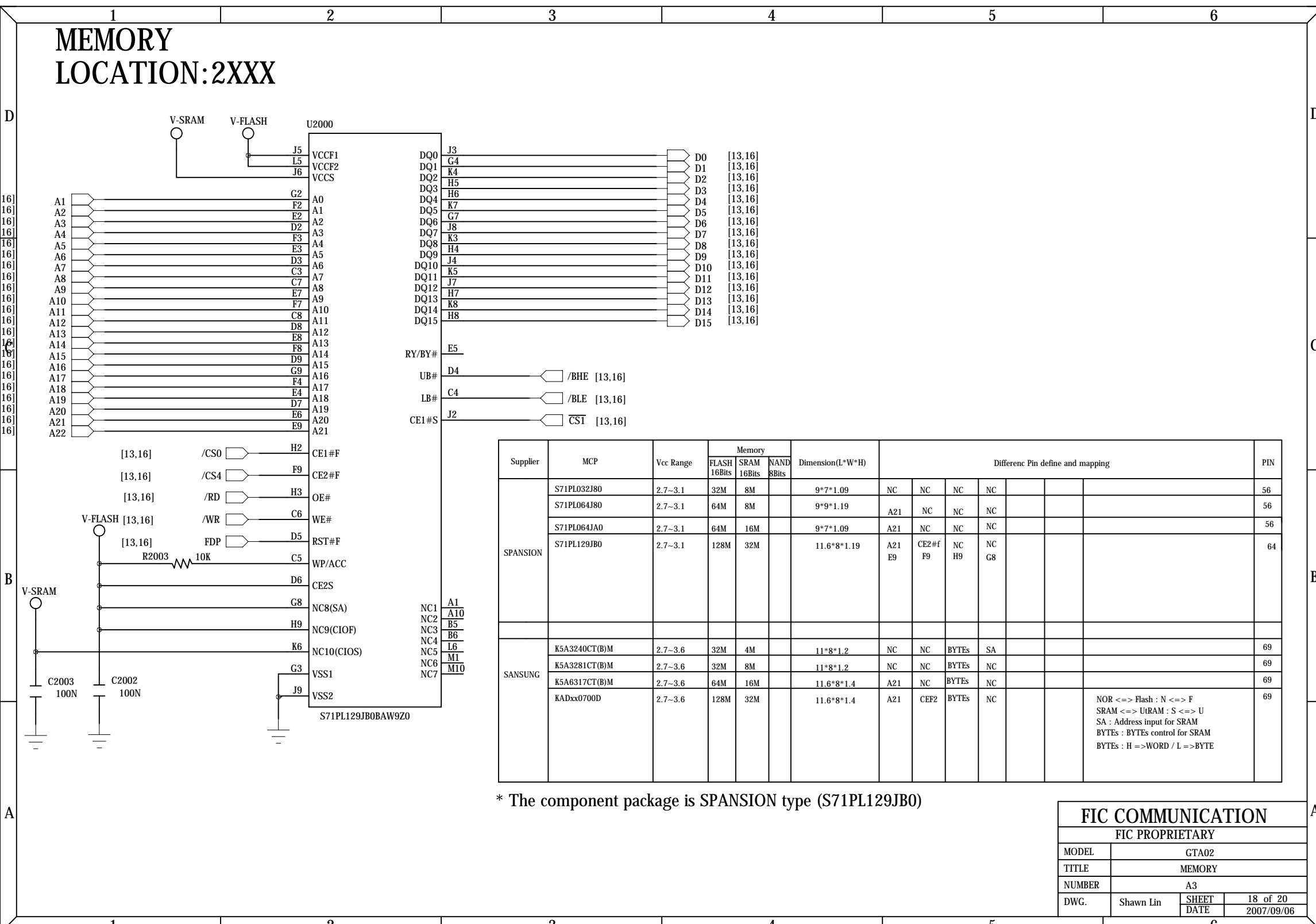
Unit:mils
Er=4.2



mode	VC1	VC2	VC3
GSM850/900_TX	0V	0V	2.6V
DCS/PCS_TX	2.6V	0V	0V
GSM850/900_RX	0V	0V	0V
DCS_RX	0V	0V	0V
PCS_RX	0V	2.6V	0V

FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	RF		
NUMBER	A3		
DWG.	Shawn Lin	SHEET	17 of 20
		DATE	2007/09/06

MEMORY LOCATION: 2XXX



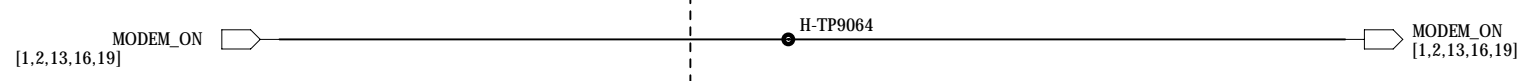
Supplier	MCP	Vcc Range	FLASH 16Bits	SRAM 16Bits	NAND 8Bits	Dimensions(L*W*H)	Differenc Pin define and mapping				PIN
SPANSION	S71PL032J80	2.7-3.1	32M	8M		9*7*1.09	NC	NC	NC	NC	56
	S71PL064J80	2.7-3.1	64M	8M		9*9*1.19	A21	NC	NC	NC	56
	S71PL064JA0	2.7-3.1	64M	16M		9*7*1.09	A21	NC	NC	NC	56
	S71PL129JB0	2.7-3.1	128M	32M		11.6*8*1.19	A21	CE2#F	NC	NC	64
SANSUNG	K5A3240CT(B)M	2.7-3.6	32M	4M		11*8*1.2	NC	NC	BYTES	SA	69
	K5A3281CT(B)M	2.7-3.6	32M	8M		11*8*1.2	NC	NC	BYTES	NC	69
	K5A6317CT(B)M	2.7-3.6	64M	16M		11.6*8*1.4	A21	NC	BYTES	NC	69
	K4Dxx0700D	2.7-3.6	128M	32M		11.6*8*1.4	A21	CE2F	BYTES	NC	69

* The component package is SPANSION type (S71PL129JB0)

FIC COMMUNICATION	
FIC PROPRIETARY	
MODEL	GTA02
TITLE	MEMORY
NUMBER	A3
DWG.	Shawn Lin
SHEET	18 of 20
DATE	2007/09/06

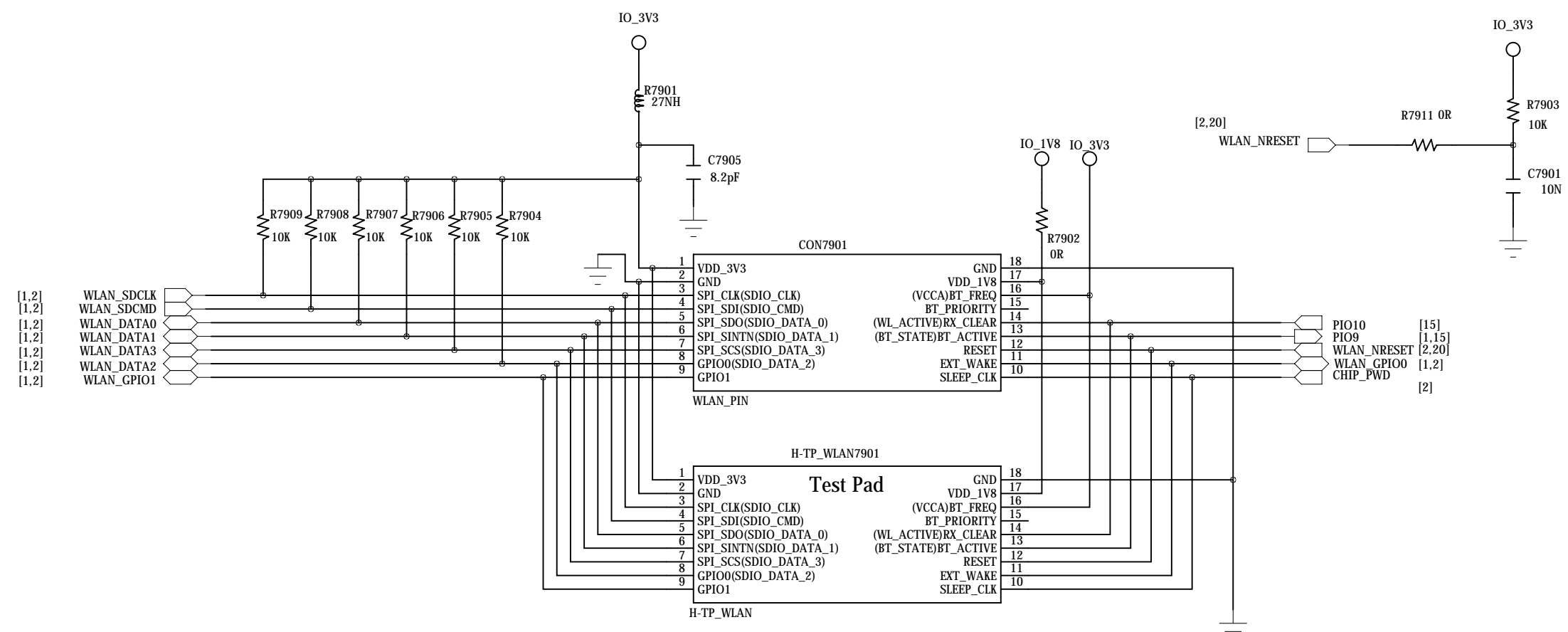
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1 2 3 4 5 6 7 8 9 10 11 12



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	MODEM INTERFACE		
NUMBER	A2		
DWG.	Shawn Lin	SHEET DATE	19 of 2007

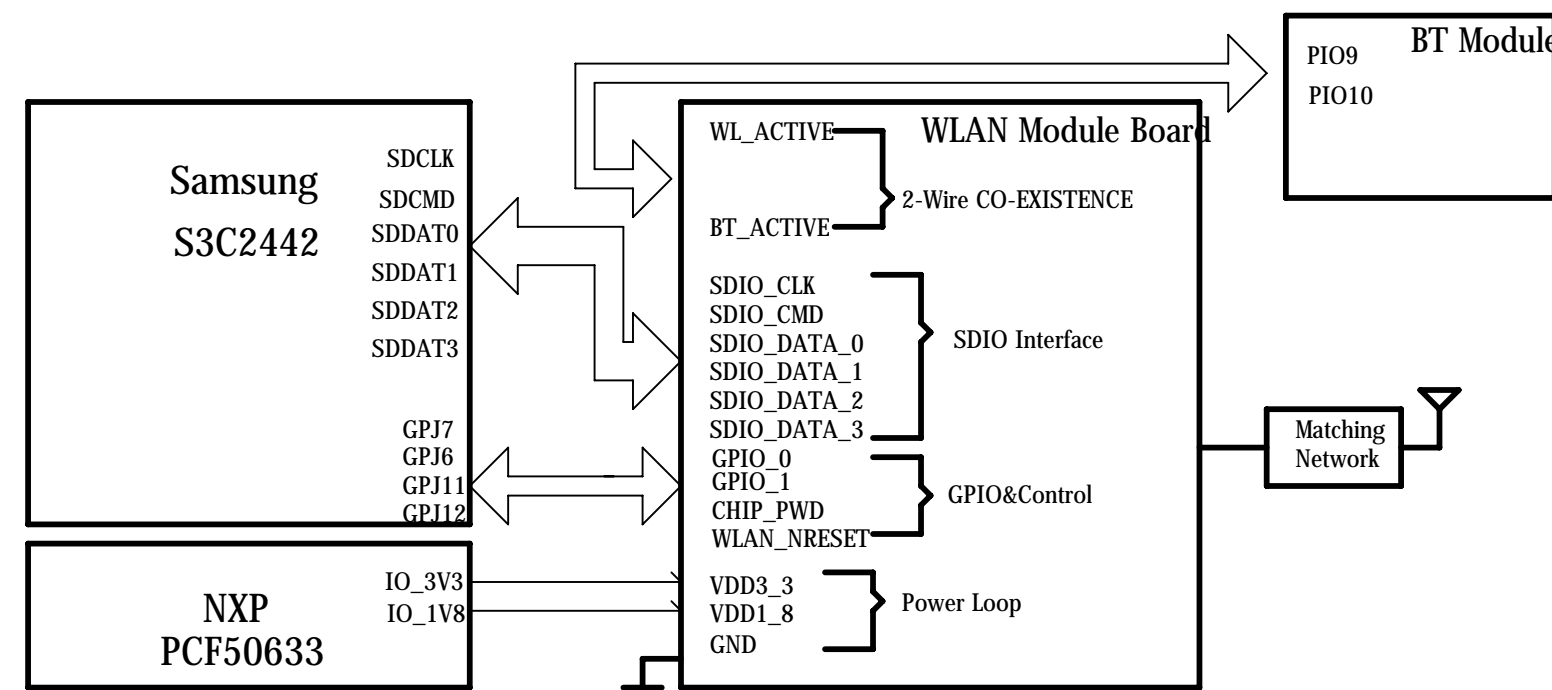
WLAN LOCATION: 79XX



Module Pin	DFWM-MHG21	DFWM-AHG11	SWL-2480	SWL-2700S
	DELTA		SAMSUNG	
	Marvell 88W8686	Atheros AR6001GZ	Marvell 88W8686	Atheros AR6001GZ
1	○	○	○	○
2	○	○	○	○
3	○	○	○	○
4	○	○	○	○
5	○	○	○	○
6	○	○	○	○
7	○	○	○	○
8	○	○	○	○
9				
10	■		■	
11	■		■	
12	■		■	
13	○	○	○	○
14	○	○	○	○
15	○	○	○	○
16	○	○	○	○
17				
18	○	○	○	○

*3-wire co-existence supported only use CS324 BT module

- Power Loop
- SPI Interface
- ▲ SDIO Interface
- Interruption Control
- 3-wire Co-existence if BT supports
- 4-wire Co-existence if BT supports



FIC COMMUNICATION			
FIC PROPRIETARY			
MODEL	GTA02		
TITLE	WLAN		
NUMBER	A3		
DWG.	Shawn Lin	SHEET	20 of 20
		DATE	2007/09/06